

Updated S
09/769,978

L Number	Hits	Search Text	DB	Time stamp
1	69644	(remov\$6 or alter\$6 or insert\$6 or add\$6 or plug\$6) with cartridge\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/05 17:33
2	45342	(remov\$6 or alter\$6 or insert\$6 or add\$6 or plug\$6) near3 cartridge\$	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/05 17:34
3	20863	power\$6-down or power\$6-up	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/05 17:35
4	22	((remov\$6 or alter\$6 or insert\$6 or add\$6 or plug\$6) near3 cartridge\$) with (power\$6-down or power\$6-up)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/05 17:35
5	23	((remov\$6 or alter\$6 or insert\$6 or add\$6 or plug\$6) with cartridge\$) with (power\$6-down or power\$6-up)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/05 17:39
6	15	((remov\$6 or alter\$6 or insert\$6 or add\$6 or plug\$6) with cartridge\$) with (power\$6-down or power\$6-up)) and (7\$/\$.ccis.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/05 17:40

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4	22	((remov\$6 or alter\$6 or insert\$6 or add\$6 or plug\$6) near3 cartridge\$) with (power\$6-down or power\$6-up)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/05 17:35
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6	15	((remov\$6 or alter\$6 or insert\$6 or add\$6 or plug\$6) with cartridge\$) with (power\$6-down or power\$6-up)) and (7\$/\$.cccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/05 17:40

US-PAT-NO: 6604152

DOCUMENT-IDENTIFIER: US 6604152 B1

TITLE: Executing program installed in computer slot in
one of plural environments comprising a main operating
system or small operating system or no operating system

----- KWIC -----

Detailed Description Text - DETX (8):

To begin, process 15 waits (201) for a cartridge to be inserted into slot

11. Once a cartridge 12 has been inserted, process 15 powers-up (202) computer

10 and reads (203) configuration data from cartridge 12. Based on this configuration data, process 15 determines (204) if program(s) stored on cartridge 12 require an SOS or an MOS. If the program(s) require an MOS,

process 15 boots (205) the MOS stored on computer 10 and launches (206) the

program(s) on cartridge 12 (see FIG. 4). If the program(s) use an SOS, process

15 switches (207) computer 10 to "appliance mode". In appliance mode, computer

10 operates as a stand-alone, single-function device and has limited functionality. For example, in appliance mode, computer 10 executes programs

on cartridge 12, performs some rudimentary processes, and little else. Process

15 boots (208) the SOS and launches (209) the program(s) on cartridge 12 (see

FIG. 5).

US-PAT-NO: 6118603

DOCUMENT-IDENTIFIER: US 6118603 A
See image for Certificate of Correction

TITLE: Disk with fault-tolerant sample-data servo
pattern

----- KWIC -----

Detailed Description Text - DETX (54):

The parallel position data signal 514 is provided to a modulation code decoder 568. In a preferred embodiment, the decoder 568 is programmable to decode either RLL or PPM encoded data signals. In such a case, the determination as to whether the decoder 568 performs RLL or PPM decoding may be made at power-up in response, for example, to a number stored in an internal register in a removable cartridge disk drive. This is consistent with programmability disclosed in connection with other circuits described above.

US-PAT-NO: 6104561

DOCUMENT-IDENTIFIER: US 6104561 A

TITLE: Read/write protect scheme for a disk cartridge
and drive

----- KWIC -----

Detailed Description Text - DETX (31):

FIG. 6 is flow diagram illustrating both the operation of the disk drive 40 of FIG. 5, particularly microprocessor 92, and a preferred embodiment of the method of the present invention. Upon power-up of the disk drive 40, the microprocessor 92 waits at step 102 until the insertion of a disk cartridge into the disk drive 40 is detected. Preferably, detection of a disk cartridge is achieved in accordance with the methods and apparatus described in co-pending application Ser. No. 08/337,678, filed Nov. 10, 1994, now U.S. Pat. No. 5,854,719, entitled "Disk Cartridge Detection Methods and Apparatus". When the insertion of a disk cartridge into the disk drive 40 is detected, control passes to step 104.

US-PAT-NO: 5644444

DOCUMENT-IDENTIFIER: US 5644444 A

TITLE: Read/write protect scheme for a disk cartridge
and drive

----- KWIC -----

Detailed Description Text - DETX (30):

FIGS. 6A and 6B are a is flow diagram illustrating both the operation of the disk drive 40 of FIG. 5, particularly microprocessor 92, and a preferred embodiment of the method of the present invention. Upon power-up of the disk drive 40, the microprocessor 92 waits at step 102 until the insertion of a disk cartridge into the disk drive 40 is detected. Preferably, detection of a disk cartridge is achieved in accordance with the methods and apparatus described in co-pending application Ser. No. 08/337,678, filed Nov. 10, 1994, entitled "Disk Cartridge Detection Methods and Apparatus" When the insertion of a disk cartridge into the disk drive 40 is detected, control passes to step 104.

US-PAT-NO: 4870605

DOCUMENT-IDENTIFIER: US 4870605 A

TITLE: Removable data cartridge for a computer system

----- KWIC -----

Detailed Description Text - DETX (20):

The locking holes are in registration when the cartridge is in the final position. When the cartridge is powered-up by the main unit, the pin extends through the locking holes to prevent the cartridge from being removed from the cavity during operation session. When the power to the cartridge is off, pin 230P retracts and the cartridge may be removed from the main unit. At the termination of each work session, the system is powered down causing pin 120I to retract into the solenoid.

US-PAT-NO: 4809697

DOCUMENT-IDENTIFIER: US 4809697 A

TITLE: Interactive programming and diagnostic system
for use with implantable pacemaker

----- KWIC -----

Detailed Description Text - DETX (15):

Also connected to the address bus 64 and the data bus 66 is non-volatile memory 72. Memory 72 is preferably realized using a programmable read only memory (ROM) device which has been programmed to contain the power-up initialization software for the microprocessor 42 and the programs that are active when the program cartridge 40 is removed from the APS-II system unit 30.